

# Jeremiah Smith

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- Objective                      Seeking employment in challenging electronics engineering position.
- Experience                      **2001-Present RF Micro Devices                      Billerica, MA**  
Design Engineer
- Design and test ICs for fiber optic data communications applications.
    - Limiting amplifier, transimpedance amplifier, laser driver.
  - Implement and use laboratory test and measurement systems.
    - Fiber alignment, laser controllers, digital communication analyzers, vector network analyzers, bit-error rate testers.
  - Use of Agilent ADS, Cadence, Protel, and AutoCAD software.
    - Circuit design and simulation, integrated circuit layout, prototype evaluation printed circuit board (PCB) design and modeling.
- 2000                      University of Michigan, Ann Arbor                      Ann Arbor, MI**  
Graduate Student Instructor
- Microwave Circuits.
    - Topics: transmission lines, S-parameters, matching networks, power dividers, directional couplers, filters, mixers, and amplifiers.
  - Supervise student labs; give recitation lectures; grade homework.
- 1999-2000                      California Institute of Technology                      Pasadena, CA**  
Teaching Assistant
- Principles of Microprocessor Systems.
    - Topics: assembly language, proper coding techniques, and debugging tactics in the context of an instructor-designed Intel 80188XL-based hardware system.
  - Microprocessor Systems Laboratory.
    - Topics: further develop assembly language, digital design, and debugging skills in the context of student-designed and -built Intel 80188XL-based digital audio recorder.
  - Introduction to Electrical Engineering.
    - Topics: complex math, Fourier series analysis, and basic circuits in the context of a series of labs using a student-modified FM radio.
- 1999                      Hughes Space & Communications Co. El Segundo, CA**  
B. S. Engineering Student
- Design, build, test, and tune C-Band (7-GHz) Low-Noise Amplifier.
- 1998                      California Institute of Technology                      Pasadena, CA**  
Summer Undergraduate Research Fellowship
- 21-MHz Class-E Power Amplifier.
  - Semifinalist in Perpall Speaking Competition.

Education	<p><b>Fall 2000      University of Michigan, Ann Arbor      Ann Arbor, MI</b></p> <ul style="list-style-type: none"> <li>▪ Electrical Engineering coursework: <ul style="list-style-type: none"> <li>▪ Monolithic Amplifier Circuits, Solid State Microwave Circuits.</li> </ul> </li> </ul> <p><b>1996-2000      California Institute of Technology      Pasadena, CA</b></p> <ul style="list-style-type: none"> <li>▪ B.S. Electrical Engineering.</li> <li>▪ Electrical Engineering coursework: <ul style="list-style-type: none"> <li>▪ Analog Electronics, Principles of Microprocessor Systems, Microprocessor Systems Laboratory, Fundamentals of Energy Processing Systems, Analog Project Laboratory, Introduction to Linear Systems, Introduction to Physics and Techniques of Remote Sensing, Introduction to Physics of Semiconductors and Semiconducting Devices, Communications Systems Fundamentals, Application of Remote Sensing in the Field, Optoelectronics and Optoelectronic Devices, Experimental Projects in Electrical Engineering, Microprocessor Project Laboratory, Microwave Circuits and Antennas.</li> </ul> </li> <li>▪ Physics and Applied Physics coursework: <ul style="list-style-type: none"> <li>▪ Classical Waves, Quantum Mechanics and Wavefunctions, Solid-State Electronics for Integrated Circuits.</li> </ul> </li> <li>▪ GPA 3.6/4.0.</li> </ul> <p><b>Fall 1999      St. John's College, Cambridge University Cambridge, U</b></p> <ul style="list-style-type: none"> <li>▪ Physics and Electrical Engineering coursework: <ul style="list-style-type: none"> <li>▪ Electrodynamics and Special Relativity; VLSI Design, Technology, and CAD; Optoelectronic Technology; Electronic Circuits.</li> </ul> </li> </ul>
Skills	<p><b>Electrical Engineering</b></p> <ul style="list-style-type: none"> <li>▪ Analog design <ul style="list-style-type: none"> <li>▪ High-speed integrated circuits.</li> <li>▪ Hybrid microwave circuits.</li> <li>▪ Power amplifiers.</li> <li>▪ FM links: VCOs and PLLs.</li> </ul> </li> <li>▪ Digital design <ul style="list-style-type: none"> <li>▪ Discrete and programmable logic devices.</li> <li>▪ Microprocessors and microcontrollers (Intel and Motorola).</li> </ul> </li> <li>▪ Controls <ul style="list-style-type: none"> <li>▪ Linear systems, LaPlace and Fourier transforms, Bode plots.</li> <li>▪ Feedback theory and implementation, circuit and system level.</li> </ul> </li> </ul> <p><b>Programming Languages</b></p> <ul style="list-style-type: none"> <li>▪ Pascal, C, C++, Assembly (Intel x86 and Motorola), PALASM, HTML.</li> </ul> <p><b>Software</b></p> <ul style="list-style-type: none"> <li>▪ Circuit design and simulation: Agilent ADS, Cadence, HSPICE, Puff.</li> <li>▪ Integrated circuit layout: Cadence.</li> <li>▪ PCB design: Protel, Puff.</li> <li>▪ CAD: AutoCAD.</li> </ul> <p><b>Mechanical</b></p> <ul style="list-style-type: none"> <li>▪ Machine shop: can use drill press, milling machine, lathe.</li> <li>▪ Experience with small engine maintenance and repair.</li> </ul>
Hobbies	Walking, hiking, reading, basketball, and cooking.